

Hands-on Introduction to Design and Verification through Verilog & System Verilog @ IEEE Student Branch CEC Mangalore

22nd April Sunday

Registration

Open to all student members & faculty from engineering colleges. At most 30 participants will be permitted. Please use the Form to register.

Space is limited 30 seats only

Register & reserve your seat

Conveyance

College Bus via a common route shall be arranged, that shall be intimated soon.

Queries

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For Details Please visit:
www.canaraengineering.in

Greetings from IEEE Student Branch CEC.

IEEE CAS Bangalore Chapter has come forward to help us organise a one day workshop for at Canara Engineering College Mangalore on the aforementioned topic. The participants will get an introduction to the topic of digital design verification. In particular, Verilog and System Verilog will be discussed and RTL coding and development of test benches will be provided. The participant must have taken a class on Digital Design and must be familiar with combinational and sequential logic design. Prior exposure to VLSI Design and VHDL or Verilog will be useful. PCs will be available to participants for carrying out hands-on exercises. However you can get your own laptops with the necessary softwares as well.

Resource person



Dr. C.P. Ravikumar

Presently the Director of Technical Talent Development at Texas Instruments, India. He leads the TI India Technical University which provides learning opportunities to employees of TI India. His earlier roles in TI include Director of University Relations and Senior Technologist (VLSI Test). He is also an adjunct faculty at IIT Madras. Before joining TI India in 2001, He was a Professor of Electrical Engineering at the IIT, Delhi (1991-2001). He also held a visiting position at the University of Southern California (1995-1996) and was Vice President (Training) at Controlnet India Pvt Ltd (2000-2001). He obtained his Ph.D. (Computer Engineering) from the University of Southern California (1991), M.E. in Computer Science with highest scores from Indian Institute of Science (1987) and B.E. in Electronics with a Gold Medal from Bangalore University (1983). He has published over 200 papers in leading International conferences and journals. He founded the VLSI Design and Test Symposium (VDAT) and was the General Chair of this event for 15 years, from its inception in 1998 to 2011. He is the author/editor/coauthor of over 12 books in areas of VLSI and has contributed several book chapters. He is the editor of the Journal of VLSI and Electronic System Design and on the editorial board of the Journal of Electronic Testing – Theory and Applications and the Journal of Low Power Electronics. He has won the best paper award at IEEE International Conference on VLSI Design (2002) and VLSI Test Symposium (2005). He is a Senior Member of IEEE, honorary secretary of IEEE CAS Bangalore chapter (2004-current) which he founded, and the honorary secretary of VLSI Society of India (2003-2011).

Register <https://go.gl/forms/fcG10KQ9f-KuufkA2>